

INTERNATIONAL STANDARD

ISO/IEC 11458

1993

AMENDMENT 1
2000-07

Amendment 1

Information technology – Microprocessor systems – VICbus – Inter-crate cable bus

Amendement 1

Technologies de l'information – Systèmes à microprocesseurs – VICbus – Bus à câbles inter-châssis

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FOREWORD

Amendment 1 to International Standard ISO/IEC 11458 was prepared by subcommittee 26: Microprocessor systems, of ISO/IEC joint technical committee 1: Information technology.

A horizontal line in the margin indicates where the text has been changed with respect to the original table.

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Table 21 – VICbus connector pin assignments

Replace the existing table 21 by the following new table:

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Table 21 – VICbus connector pin assignments

Connectors VC1-i and VC1-o			
Pin	Line	Pin	Line
1	AP+	2	AP–
3	ALOCK+	4	ALOCK–
5	WAIT+	6	WAIT–
7	SERR+	8	SERR–
9	ID0+	10	ID0–
11	ID1+	12	ID1–
13	ID2+	14	ID2–
15	ID3+	16	ID3–
17	CL3+	18	CL3–
19	CL2+	20	CL2–
21	CL0+	22	CL0–
23	CL1+	24	CL1–
25	INT0+	26	INT0–
27	INT1+	28	INT1–
29	INT2+	30	INT2–
31	INT3+	32	INT3–
33	AD00+	34	AD00–
35	AD01+	36	AD01–
37	AD02+	38	AD02–
39	AD03+	40	AD03–
41	AD04+	42	AD04–
43	AD05+	44	AD05–
45	AD06+	46	AD06–
47	AD07+	48	AD07–
49	AD08+	50	AD08–
51	AD09+	52	AD09–
53	AD10+	54	AD10–
55	AD11+	56	AD11–
57	AD12+	58	AD12–
59	AD13+	60	AD13–
61	AD14+	62	AD14–
63	AD15+	64	AD15–

Connectors VC2-i and VC2-o			
Pin	Line	Pin	Line
1	ID4+	2	ID4–
3	BGLOOP+	4	BGLOOP–
5	AS+	6	AS–
7	DS+	8	DS–
9	BBSY+	10	BBSY–
11	INTSEL0+	12	INTSEL0–
13	VICRESET+	14	VICRESET–
15	BR+	16	BR–
17	GROUND	18	GROUND
19	DEVFAIL+	20	DEVFAIL–
21	BGIN+ (VC2-i) BGOUT+ (VC2-o)	22	BGIN– (VC2-i) BGOUT– (VC2-o)
23	INTSEL1+	24	INTSEL1–
25	INT4+	26	INT4–
27	INT5+	28	INT5–
29	INT6+	30	INT6–
31	INT7+	32	INT7–
33	AD16+	34	AD16–
35	AD17+	36	AD17–
37	AD18+	38	AD18–
39	AD19+	40	AD19–
41	AD20+	42	AD20–
43	AD21+	44	AD21–
45	AD22+	46	AD22–
47	AD23+	48	AD23–
49	AD24+	50	AD24–
51	AD25+	52	AD25–
53	AD26+	54	AD26–
55	AD27+	56	AD27–
57	AD28+	58	AD28–
59	AD29+	60	AD29–
61	AD30+	62	AD30–
63	AD31+	64	AD31–