

INTERNATIONAL
STANDARD

IEC
60191-6-13

First edition
2007-06

**Mechanical standardization of
semiconductor devices –**

**Part 6-13:
Design guideline of open-top-type sockets for
Fine-pitch Ball Grid Array and Fine-pitch Land
Grid Array (FBGA/FLGA)**

IECNORM.COM : Click to view the full IEC 60191-6-13:2007



Reference number
IEC 60191-6-13:2007(E)



THIS PUBLICATION IS COPYRIGHT PROTECTED

Copyright © 2007 IEC, Geneva, Switzerland

All rights reserved. Unless otherwise specified, no part of this publication may be reproduced or utilized in any form or by any means, electronic or mechanical, including photocopying and microfilm, without permission in writing from either IEC or IEC's member National Committee in the country of the requester.

If you have any questions about IEC copyright or have an enquiry about obtaining additional rights to this publication, please contact the address below or your local IEC member National Committee for further information.

IEC Central Office
3, rue de Varembé
CH-1211 Geneva 20
Switzerland
Email: inmail@iec.ch
Web: www.iec.ch

About the IEC

The International Electrotechnical Commission (IEC) is the leading global organization that prepares and publishes International Standards for all electrical, electronic and related technologies.

About IEC publications

The technical content of IEC publications is kept under constant review by the IEC. Please make sure that you have the latest edition, a corrigenda or an amendment might have been published.

- Catalogue of IEC publications: www.iec.ch/searchpub

The IEC on-line Catalogue enables you to search by a variety of criteria (reference number, text, technical committee,...). It also gives information on projects, withdrawn and replaced publications.

- IEC Just Published: www.iec.ch/online_news/justpub

Stay up to date on all new IEC publications. Just Published details twice a month all new publications released. Available on-line and also by email.

- Customer Service Centre: www.iec.ch/webstore/custserv

If you wish to give us your feedback on this publication or need further assistance, please visit the Customer Service Centre FAQ or contact us:

Email: csc@iec.ch

Tel.: +41 22 919 02 11

Fax: +41 22 919 03 00

IECNORM.COM

: Click to view the IECNORM.COM

INTERNATIONAL
STANDARD

IEC
60191-6-13

First edition
2007-06

**Mechanical standardization of
semiconductor devices –**

**Part 6-13:
Design guideline of open-top-type sockets for
Fine-pitch Ball Grid Array and Fine-pitch Land
Grid Array (FBGA/FLGA)**

IECNORM.COM : Click to view the full IEC 60191-6-13:2007



Commission Electrotechnique Internationale
International Electrotechnical Commission
Международная Электротехническая Комиссия

CONTENTS

FOREWORD	3
1 Scope	5
2 Normative references	5
3 Terms and definitions	5
4 Socket code	5
4.1 Construction of socket code	5
4.2 Symbols	6
5 Terminal number	6
6 Socket nominal dimension	6
7 Socket length and width	7
8 Reference symbols and schematics	7
8.1 Outline drawings	7
8.2 Reference symbols and schematics of recommended socket mounting pattern on printed circuit board	9
8.3 Overall dimensions	10
8.4 Recommended dimensions of socket mounting pattern on printed circuit board	14
9 Individual outline drawing standard registration	15
Table 1 – Overall dimensions	10
Table 2 – Socket dimensions	12
Table 2a – Socket dimensions for Group 1, 2 and 3 (square socket)	12
Table 2b – Socket dimension for Group 4 (square or rectangular socket)	13
Table 3 – Socket mounting dimensions	14
Table 4 – Registration table	15
Figure 1 – Outline drawings of the socket	8
Figure 2 – Applicable package outline	8
Figure 3 – Socket mounting pattern	9

INTERNATIONAL ELECTROTECHNICAL COMMISSION

MECHANICAL STANDARDIZATION OF SEMICONDUCTOR DEVICES –

Part 6-13: Design guideline of open-top-type sockets for Fine-pitch Ball Grid Array and Fine-pitch Land Grid Array (FBGA/FLGA)

FOREWORD

- 1) The International Electrotechnical Commission (IEC) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of IEC is to promote international co-operation on all questions concerning standardization in the electrical and electronic fields. To this end and in addition to other activities, IEC publishes International Standards, Technical Specifications, Technical Reports, Publicly Available Specifications (PAS) and Guides (hereafter referred to as "IEC Publication(s)"). Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International, governmental and non-governmental organizations liaising with the IEC also participate in this preparation. IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
- 2) The formal decisions or agreements of IEC on technical matters express, as nearly as possible, an international consensus of opinion on the relevant subjects since each technical committee has representation from all interested IEC National Committees.
- 3) IEC Publications have the form of recommendations for international use and are accepted by IEC National Committees in that sense. While all reasonable efforts are made to ensure that the technical content of IEC Publications is accurate, IEC cannot be held responsible for the way in which they are used or for any misinterpretation by any end user.
- 4) In order to promote international uniformity, IEC National Committees undertake to apply IEC Publications transparently to the maximum extent possible in their national and regional publications. Any divergence between any IEC Publication and the corresponding national or regional publication shall be clearly indicated in the latter.
- 5) IEC provides no marking procedure to indicate its approval and cannot be rendered responsible for any equipment declared to be in conformity with an IEC Publication.
- 6) All users should ensure that they have the latest edition of this publication.
- 7) No liability shall attach to IEC or its directors, employees, servants or agents including individual experts and members of its technical committees and IEC National Committees for any personal injury, property damage or other damage of any nature whatsoever, whether direct or indirect, or for costs (including legal fees) and expenses arising out of the publication, use of, or reliance upon, this IEC Publication or any other IEC Publications.
- 8) Attention is drawn to the Normative references cited in this publication. Use of the referenced publications is indispensable for the correct application of this publication.
- 9) Attention is drawn to the possibility that some of the elements of this IEC Publication may be the subject of patent rights. IEC shall not be held responsible for identifying any or all such patent rights.

International Standard IEC 60191-6-13 has been prepared by subcommittee 47D: Mechanical standardization for semiconductor devices, of IEC technical committee 47: Semiconductor devices.

The text of this standard is based on the following documents:

FDIS	Report on voting
47D/681/FDIS	47D/692/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all the parts in the IEC 60191 series, under the general title *Mechanical standardization of semiconductor devices*, can be found on the IEC website.

The committee has decided that the contents of this publication will remain unchanged until the maintenance result date indicated on the IEC web site under "http://webstore.iec.ch" in the data related to the specific publication. At this date, the publication will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

A bilingual version of this publication may be issued at a later date.

IECNORM.COM : Click to view the full PDF of IEC 60191-6-13:2007

MECHANICAL STANDARDIZATION OF SEMICONDUCTOR DEVICES –

Part 6-13: Design guideline of open-top-type sockets for Fine-pitch Ball Grid Array and Fine-pitch Land Grid Array (FBGA/FLGA)

1 Scope

This part of IEC 60191 gives a design guideline of open-top-type semiconductor sockets for Fine-pitch Ball Grid Array (“FBGA” hereafter) and Fine-pitch Land Grid Array (“FLGA” hereafter). This standard is intended to establish the outline drawings and dimensions of the open-top-type socket out of the test and burn-in sockets applied to FBGA and FLGA.

2 Normative references

The following referenced documents are indispensable for the application of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60191-2, *Mechanical standardization of semiconductor devices – Part 2: Dimensions*

IEC 60191-6:2004, *Mechanical standardization of semiconductor devices – Part 6: General rules for the preparation of outline drawings of surface mounted semiconductor device packages*

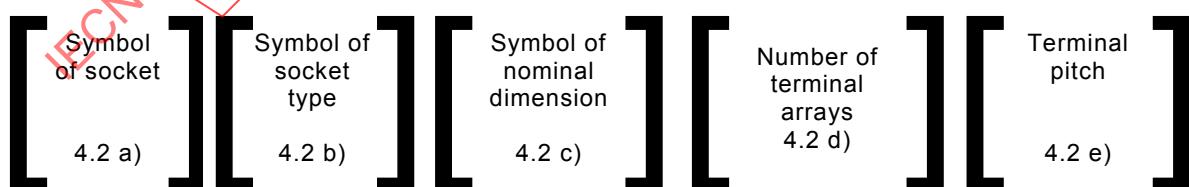
3 Terms and definitions

For the purposes of this document, the terms and definitions of IEC 60191-6 apply.

4 Socket code

4.1 Construction of socket code

A socket code is constructed as follows.



Example **SFB**

TX

2120AB

1616

080

4.2 Symbols

a) Semiconductor sockets symbol

The symbol for socket shall be expressed in 3 characters. The first character, “S”, refers to socket and the rest to the package code. FBGA shall be expressed as “FB”, FLGA shall be expressed as “FL”.

b) Socket type symbol

The symbol for socket type shall be expressed in 2 characters. The first character “T” refers to open top type and the rest remains option “X”. Clamshell type socket is referred to as “C”.

c) Socket nominal dimension symbol

The symbol for nominal dimension shall be expressed in 6 characters, which are 4 numeric characters and 2 alphabetical characters. The first 4 numeric characters comply with nominal dimension $E \times D$ which refers to applicable maximum width and length of FBGA/FLGA package.

The last 2 alphabetical characters refer to socket base matrix size either an even or an odd.

It refers to an odd contact row by “A” and an even contact row by “B” in order socket width direction and next socket length direction.

Namely, it refers to “AA” in case row number is an odd both for width and length direction, “BB” in case row number is an even both for width and length direction, “AB” in case row number is an odd at width direction and an even at length direction and “BA” in case row number is an even at width direction and an odd at length direction.

d) Number of terminal arrays

The symbol for number of terminal arrays shall be expressed by 4 numeric characters applying applicable package matrix size in E direction and D direction.

e) Terminal pitch

The symbol for terminal pitch of applicable package shall be expressed in 3 numeric characters. A decimal [.] is omitted.

5 Terminal number

The terminal number is provided in the following manner when the socket is viewed with the angle from topside. The horizontal row nearest to the index corner when the index is placed on the left topside is referred to as A.

As the row moves down, the number changes in the order of B, C, AA, AB.

1 is defined for the vertical row nearest to the index corner. As the row moves rightward, the number is increased 2, 3, The terminal number is combined with these alphabets and numbers and expressed as A1 or B1. I, O, Q, S, X and Z are not used as symbols for a horizontal row.

6 Socket nominal dimension

The applicable package length and width which extend from 1,50 mm to 21,0 mm by 0,50 mm increments are divided into 4 package groups. The socket nominal dimension is defined by the largest value of the package length or width in each socket group.

In consideration of a specific need for minimum socket outline size, the socket nominal dimension with 1,0 mm increments can be specified as an exception. Package length and width of 5,00 mm or less is unified in one socket nominal dimension.

7 Socket length and width

Socket length and width are categorized into 4 groups, from group 1 to group 4, to cover the difference of its terminal count and mechanism.

In socket group 1, 2 and 3, only square socket outline is allowed. Socket length and width are determined by the nominal dimension value plus 36,0 mm, 24,0 mm and 12,0 mm respectively.

In socket group 4, square and rectangular socket outlines are allowed. Socket length and width are determined by the nominal dimension value plus 8,0 mm independently in each sides.

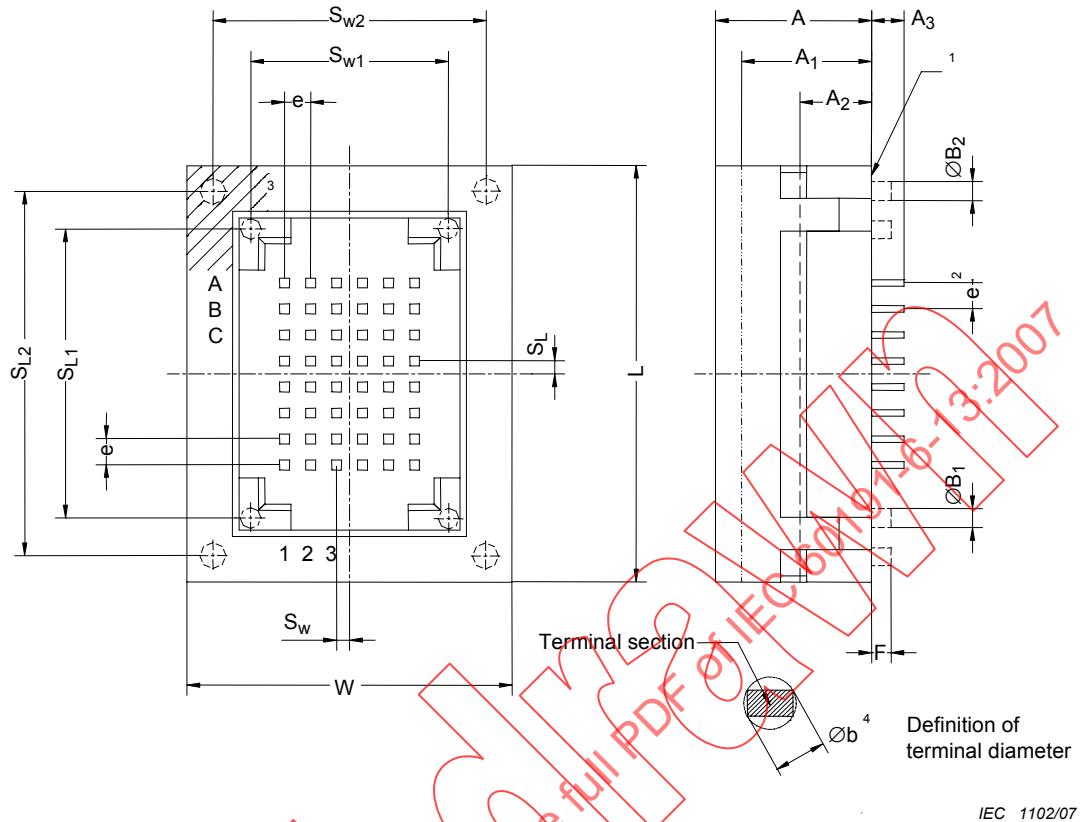
Socket group 1 aims for high terminal count package or FLGA socket which requires complicated socket structure. Socket group 2 and 3 are for the socket currently available. Socket group 4 is for the socket which is required to have the smallest possible outline such as for Memory IC.

Socket group number	Allowed socket outline	To determine socket length and width, the following values are added to the socket nominal dimension
Group 1	Square	36 mm
Group 2	Square	24 mm
Group 3	Square	12 mm
Group 4	Square or rectangular	8 mm

8 Reference symbols and schematics

8.1 Outline drawings

Outline drawings of the socket are shown in Figure 1 and the applicable package outline is in Figure 2. The overall dimensions are in Table 1. Socket dimensions are given in Table 2.



- (¹) Indicates mounting plane. Mounting plane is defined by the plane where the socket contacts its mounting surface.
- (²) Stipulates true geometric position of the terminals.
- (³) Indicates positional tolerance of the index mark. Index mark should be completely within the shaded area.
- (⁴) Terminal diameter is defined as the maximum diameter of a circle circumscribed about a vertical projection of the terminal from the mounting plane.

Figure 1 – Outline drawings of the socket

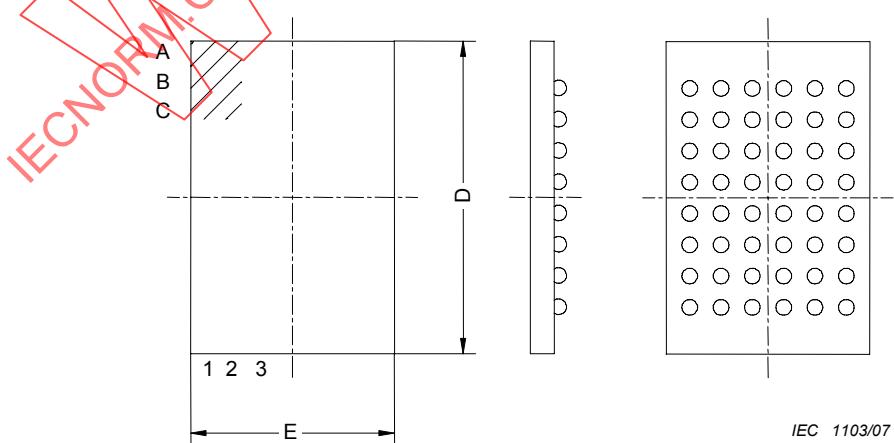


Figure 2 – Applicable package outline

8.2 Reference symbols and schematics of recommended socket mounting pattern on printed circuit board

The drawing of the recommended socket mounting pattern on a printed circuit board is shown in Figure 3 for reference in printed circuit board designing. See Table 3 for recommended dimensions.

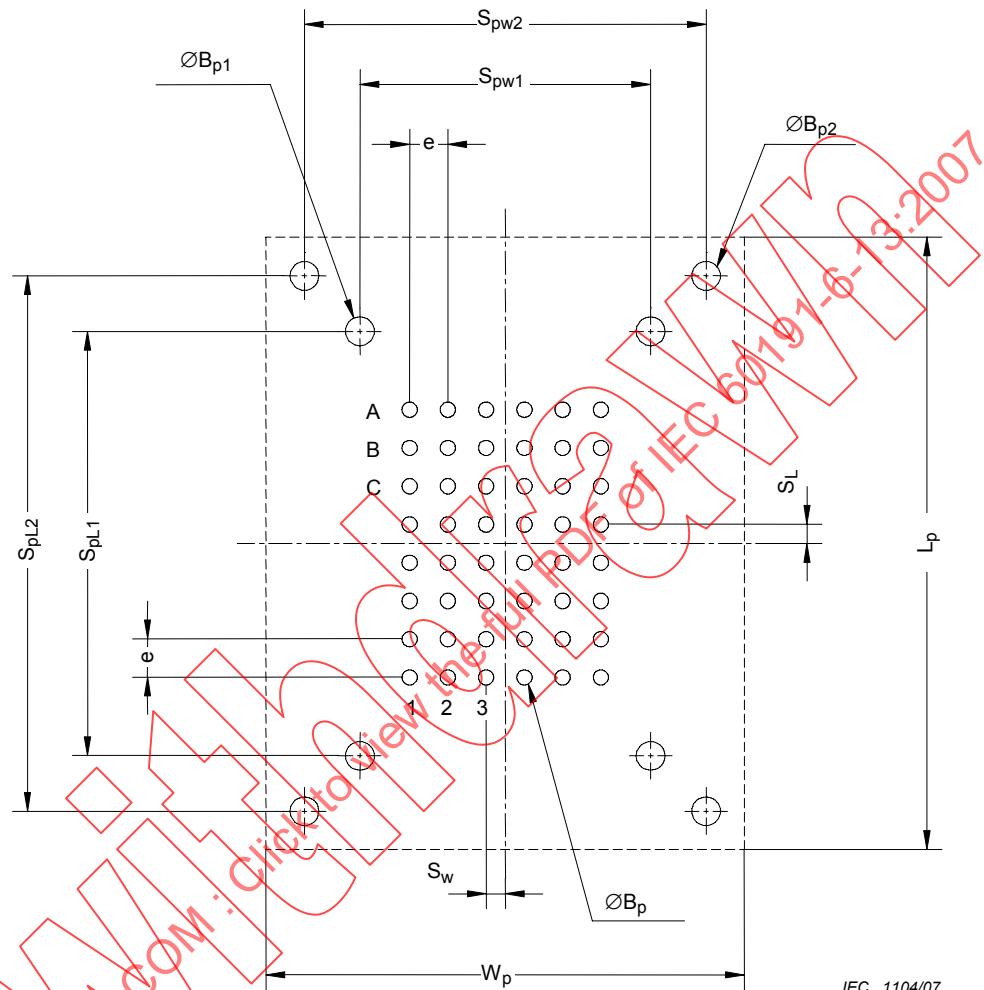


Figure 3 – Socket mounting pattern

8.3 Overall dimensions

Table 1 – Overall dimensions

Name	Reference symbol	Stipulations mm	Recommended value mm	Supple- ment										
Socket nominal dimension	$E \times D$	This value is based on the nominal dimensions of conformable FBGA and FLGA to the socket.	–	Table 2										
Socket length	L	Socket length: L nominal defined. L = W (group 4 is exception.)	–	Table 2										
Socket width	W	Socket width: W nominal defined W = L (group 4 is exception.)	–	Table 2										
Socket height	A	A max = 22,0	–											
End stroke height	A_1	A_1 max = 16,0	14,0 13,5											
Seating plane Height	A_2	A_2 max = 14,0	9,7 8,2											
Terminal pitch	e	e = 0,80 e = 0,65 e = 0,50 e = 0,40	–											
Terminal length	A_3	A_3 = 0,7 to 6,3	–											
Terminal diameter	$\emptyset b$	Maximum distance of the terminal cross-section	–											
		<table border="1"> <thead> <tr> <th>e</th> <th>$\emptyset b$ max</th> </tr> </thead> <tbody> <tr> <td>0,80</td> <td>0,28</td> </tr> <tr> <td>0,65</td> <td>0,21</td> </tr> <tr> <td>0,50</td> <td>0,20</td> </tr> <tr> <td>0,40</td> <td>0,19</td> </tr> </tbody> </table>	e	$\emptyset b$ max	0,80	0,28	0,65	0,21	0,50	0,20	0,40	0,19		
e	$\emptyset b$ max													
0,80	0,28													
0,65	0,21													
0,50	0,20													
0,40	0,19													
Number of alignment pin (inside)	n_1	n_1 = 0, 2, 3, 4 (either one to be selected)	–											
Number of alignment pin (outside)	n_2	n_2 = 0, 2, 3, 4 (either one to be selected)	–											
Alignment pin length	F	$F_{min} = 1,0$	–											
Distance between alignment pin in L-direction (inside)	S_{L1}	Group 1, 2, 3 = Socket nominal dimension plus 5,0 Group 4 = No pin exist	–	Table 2										
Distance between alignment pin in W-direction (inside)	S_{W1}	Group 1, 2, 3 = Socket nominal dimension plus 5,0 Group 4 = No pin exist	–	Table 2										

Table 1 (continued)

Name	Reference symbol	Stipulations mm	Recommended value mm	Supple- ment
Distance between alignment pin in L-direction (outside)	S_{L2}	Group 1 = Socket nominal dimension plus 30,0 Group 2 = Socket nominal dimension plus 18,0 Group 3 = Socket nominal dimension plus 9,0 Group 4 = Socket nominal dimension plus 5,0	–	Table 2
Distance between alignment pin in W-direction (outside)	S_{W2}	Group 1 = Socket nominal dimension plus 30,0 Group 2 = Socket nominal dimension plus 18,0 Group 3 = Socket nominal dimension plus 9,0 Group 4 = Socket nominal dimension plus 5,0	–	Table 2
Alignment pin diameter (inside)	$\emptyset B_1$	$\emptyset B_1$ max = 1,5	–	
Alignment pin diameter (outside)	$\emptyset B_2$	Group 1 and 2 = $\emptyset B_2$ max = 2,0 Group 3 and 4 = $\emptyset B_2$ max = 1,5	–	Table 2
Centre terminal position in L-direction	S_L	When M_L is an odd number, $S_L = 0$ When M_L is an even number, $S_L = e / 2$	–	
Centre terminal position in W-direction	S_W	When M_W is an odd number, $S_W = 0$ When M_W is an even number, $S_W = e / 2$	–	
Number of terminals	N	Number of terminals and matrix sizes shall be equal to the applicable package's which is specified in IEC 60191-2.	–	
Matrix size in L-direction	M_L	Matrix layout with partially depopulated terminal is accepted.		
Matrix size in W-direction	M_W			
Package setting direction		Direction of shifting for package insertion. This is to provide the direction of package shifting in order to ensure uniformity when fitting a package to a socket that has a larger terminal matrix than the package, when that package has an odd number of rows less than the socket. The direction of shifting shall be upper left.	–	

IECNORM.COM: CINTO TO TULPDE & IEC 60191-6-13:2007

Table 2 – Socket dimensions**Table 2a – Socket dimensions for Group 1, 2 and 3 (square socket)**

Longer side of package length or width mm	Socket nominal dimension E × D mm	Socket length and width mm		
		Group 1	Group 2	Group 3
		L=W	L=W	L=W
1,50				
2,00				
2,50				
3,00				
3,50				
4,00				
4,50				
5,00				
5,50				
6,00				
6,50				
7,00				
7,50				
8,00				
8,50				
9,00				
9,50				
10,00				
10,50				
11,00				
11,50				
12,00				
12,50				
13,00				
13,50				
14,00				
14,50				
15,00				
15,50				
16,00				
16,50				
17,00				
17,50				
18,00				
18,50				
19,00				
19,50				
20,00				
20,50				
21,00				
	9×9	45,0	33,0	21,0
	13×13	49,0	37,0	25,0
	17×17	53,0	41,0	29,0
	21×21	57,0	45,0	33,0